

2	BRS	L2	17183	PLL or DLL	USPA T	2002/09/2 4 16:11	
3	BRS	L3	27131 3	lock	USPA T	2002/09/2 4 16:11	
4	BRS	L4	6789	2 and 3	USPA T	2002/09/2 4 16:12	
5	BRS	L5	6361	"charge pump"	USPA T	2002/09/2 4 16:12	
6	BRS	L6	886	4 and 5	USPA T	2002/09/2 4 17:09	
7	BRS	L7	2743	"phase locking"	USPA T	2002/09/2 4 17:10	
8	BRS	L8	692	2 and 7	USPA T	2002/09/2 4 17:10	
9	BRS	L9	103	8 and 5	USPA T	2002/09/2 4 17:15	
10	BRS	L10	5695	"frequency synthesizer"	USPA T	2002/09/2 4 17:19	
11	BRS	L11	1636	2 and 10	USPA T	2002/09/2 4 17:19	
12	BRS	L12	318	11 and 5	USPA T	2002/09/2 4 17:19	
13	BRS	L13	17	12 and 7	USPA T	2002/09/2 4 17:19	
14	BRS	L14	202	3 and 12	USPA T	2002/09/2 4 17:20	
15	BRS	L15	67479 7	detect\$	USPA T	2002/09/2 4 17:21	
16	BRS	L16	66384 2	switch\$	USPA T	2002/09/2 4 17:22	
17	BRS	L17	30634 2	15 and 16	USPA T	2002/09/2 4 17:24	
18	BRS	L18	7285	"impedance state"	USPA T	2002/09/2 4 17:24	
19	BRS	L19	30097	"high impedance"	USPA T	2002/09/2 4 17:24	
20	BRS	L20	12011	19 and 17	USPA T	2002/09/2 4 17:25	
21	BRS	L21	29	20 and 8	USPA T	2002/09/2 4 17:25	
22	BRS	L22	31	20 and 14	USPA T	2002/09/2 4 17:26	